

CS 3290 - Computer Organization and Architecture II

The textbook is a recent revision of Floyd book 8th Edition (almost same cover) same topics without VHDL

Course Information: [Course Outline](#)

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Lab Assignments:

Laboratory Assignments	
Lab No	Week of
1	Lab Assign 1
2	Lab Assign 2
3	Lab Assign 3
4	Lab Assign 4
5	Lab Assign 5
6	Lab Assign 6
7	Lab Assign 7
8	Lab Assign 8
9	Lab Assign 9
10	Lab Assign 10
11	Lab Assign 11
12	Lab Assign 12
13	Lab Assign 13

Homework:

Homework No	Due Date

Additional Resources:

The [University of New Mexico](#) offers a course which is very close to ours in content, lecture materials and exam questions (they have several old exams available).

Our textbook has a [website](#) where you find chapter overviews and various types of end of chapter questions.

Also look at the chapter summaries and some of the multiple choice questions from a similar book:

- Zipped copy of the [course/book transparencies](#) from a pretty good book by Randy Katz, Contemporary Logic Design
- LogicWorks 3 by [Capilano Computing Systems](#), the most recent download [patch](#) know as Version 3.03
- Craig Maynaird (faculty at SAIT) similar course [check it out](#)
- Texas Instruments Spec sheets: <http://www.ti.com/sc/docs/dsps/dsphome.htm>
- Motorola Spec sheets: <http://mot-sps.com/>